

PATENT
DOCKET NO.: 13596/1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Katsuhiko NISHIWAKI

SERIAL NO. : 10/562,839

FILED : 29 December 2005

FOR : SEMICONDUCTOR DEVICE HAVING A HIGH WITHSTAND
VOLTAGE

Patent No. : **7,579,652 B2** Issued 25 August 2009

CERTIFICATE OF CORRECTION BRANCH

COMMISSIONER FOR PATENTS

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REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 C.F.R §1.323

SIR:

It is respectfully requested that the enclosed certificate of correction be issued for the above Patent under authority of 35 USC §355.

The change represents correction of a spelling error in column 12 which was not previously discovered.. \$100.00 to cover the fee under 37 C.F.R. 1.20(a) is being paid by credit card. If any additional fee is required, please charge Deposit Account No. 11-0600.

Respectfully submitted,

Dated: 12 January 2009

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 7,579,652 B2 Page 1 of 1
DATED : 25 August 2009
INVENTOR(S) : Katsuhiko NISHIWAKI

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>Column</u>	<u>Line</u>	
12	27	Change "principle" to --principal--.

MAILING ADDRESS OF SENDER:

Patent No.: 7,579,652 B2

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